

Figure 6. Circuit for Sum

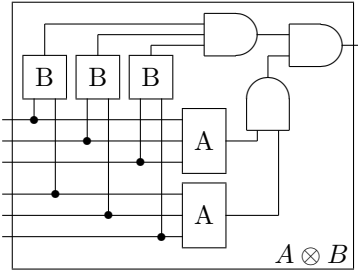


Figure 7. Circuit for Tensor Product